

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Dunlop et al.
Case: 13-6
Patent No.: 7,010,077
Issue Date: March 7, 2006
Group: 2634
Examiner: Eva Y. Zheng

Title: Gated Clock Recovery Circuit

CITATION OF PRIOR ART UNDER 37 C.F.R. §1.501

Commissioner of Patents
P O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56 and 1.501, Patentees' attorney wishes to bring to the attention of the Patent and Trademark Office the following documents. The documents are submitted on behalf of the patent owner. A copy of each listed document is enclosed, along with a copy of a corresponding Japanese Office Action dated February 28, 2007. The office action provides an explanation of the pertinency and applicability of the submitted documents. A copy of the listed item is enclosed.

Foreign Documents

1 EP 0 654 907 A1

In the event of any non-payment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit **Deposit Account No. 50-0762** as required to correct the error.

Respectfully submitted,



Date: June 29, 2007

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